

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	31	((verification or verif\$3 or validation or valida\$4) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (fault) and (((power adj suppl\$3) near4 current\$1) near\$3 test\$3)).ab.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/05 11:31
S2	1	((verification or verif\$3 or validation or valida\$4) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (fault) and (power adj suppl\$3)).ab.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/10/28 17:07
S3	931	((verification or verif\$3 or validation or valida\$4) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (fault) and (power adj suppl\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/10/28 17:07
S4	30	(verification or verif\$3 or validation or valida\$4) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (fault) and ((power adj suppl\$3) same (transient\$1 and current\$1 and test\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/10/28 17:43
S5	0	((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) near4 current\$1) near\$3 (transient\$2 or spike\$1 or surge\$1))).ab.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/10/28 17:51
S6	342	((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) near4 current\$1) near\$3 (transient\$2 or spike\$1 or surge\$1)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/10/28 17:59
S7	259	((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) near4 current\$1) near\$3 (transient\$2 or spike\$1 or surge\$1)))) and @ad<"20000124"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/10/28 17:59

S8	57	(((((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) near4 current\$1) near\$3 (transient\$2 or spike\$1 or surge\$1)))) and @ad<"20000124") and (logic adj signal\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/10/28 17:59
S9	1	(((((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) near4 current\$1) near\$3 (transient\$2 or spike\$1 or surge\$1)))) and @ad<"20000124") and (logic adj signal\$1)) and (((power adj suppl\$3) near4 current\$1) near\$3 (transient\$2 or spike\$1 or surge\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/10/28 17:58
S10	7	(((((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) near4 current\$1) near\$3 (transient\$2 or spike\$1 or surge\$1)))) and @ad<"20000124") and (logic adj signal\$1)) and (((power adj suppl\$3) same current\$1) same (transient\$2 or spike\$1 or surge\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/10/28 17:59
S11	36	((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) same current\$1) same (transient\$2 or spike\$1 or surge\$1))))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/10/28 17:59
S12	24	(((((fault adj (test\$3 or simulat\$4)) and (test near4 (vector\$1 or pattern\$1 or sequence\$1 or list\$1)) and (((power adj suppl\$3) same current\$1) same (transient\$2 or spike\$1 or surge\$1)))) and @ad<"20000124"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/10/28 17:59
S13	9	"5483170".uref.	USPAT	OR	OFF	2003/11/02 19:22
S14	8	"5483170".uref. and @ad<"20000124"	USPAT	OR	OFF	2003/11/02 19:38
S15	4	("5483170".uref. and @ad<"20000124") and transient	USPAT	OR	OFF	2003/11/02 19:57

S16	1	"5850404".pn.	USPAT	OR	OFF	2003/11/02 19:57
S17	1	"5321354".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/05 11:52
S18	1	"5321354".pn. and transient	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/05 17:05
S19	234	702/58.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/05 17:07
S20	151	702/118.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/05 17:07
S21	130	702/120.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2003/11/05 17:07
S22	424	714/719.ccls.	USPAT	OR	OFF	2003/11/05 17:08
S23	153	714/720.ccls.	USPAT	OR	OFF	2003/11/05 17:08
S24	161	714/721.ccls.	USPAT	OR	OFF	2003/11/05 17:09
S25	64	714/728.ccls.	USPAT	OR	OFF	2003/11/05 17:09
S26	226	714/735.ccls.	USPAT	OR	OFF	2003/11/05 17:09
S27	596	714/736.ccls.	USPAT	OR	OFF	2003/11/05 17:09
S28	70	714/737.ccls.	USPAT	OR	OFF	2003/11/05 17:09
S29	111	714/741.ccls.	USPAT	OR	OFF	2003/11/05 17:13
S30	2033	702/58.ccls. or 702/118.ccls. or 702/120.ccls. or 714/719.ccls. or 714/720.ccls. or 714/721.ccls. or 714/728.ccls. or 714/735.ccls. or 714/736.ccls. or 714/737.ccls. or 714/741.ccls.	USPAT	OR	OFF	2003/11/05 17:13
S31	594	(702/58.ccls. or 702/118.ccls. or 702/120.ccls. or 714/719.ccls. or 714/720.ccls. or 714/721.ccls. or 714/728.ccls. or 714/735.ccls. or 714/736.ccls. or 714/737.ccls. or 714/741.ccls.) and (simulation or emulation or model\$5)	USPAT	OR	OFF	2003/11/05 17:47

S32	0	((702/58.ccls. or 702/118.ccls. or 702/120.ccls. or 714/719.ccls. or 714/720.ccls. or 714/721.ccls. or 714/728.ccls. or 714/735.ccls. or 714/736.ccls. or 714/737.ccls. or 714/741.ccls.) and (simulation or emulation or model\$5)) and (transient\$1 with power with suppl\$3 with current)	USPAT	OR	OFF	2003/11/05 17:16
S33	9	((702/58.ccls. or 702/118.ccls. or 702/120.ccls. or 714/719.ccls. or 714/720.ccls. or 714/721.ccls. or 714/728.ccls. or 714/735.ccls. or 714/736.ccls. or 714/737.ccls. or 714/741.ccls.) and (simulation or emulation or model\$5)) and ((transient\$1) same (power and suppl\$3 and current\$1))	USPAT	OR	OFF	2003/11/05 17:17
S34	12	((702/58.ccls. or 702/118.ccls. or 702/120.ccls. or 714/719.ccls. or 714/720.ccls. or 714/721.ccls. or 714/728.ccls. or 714/735.ccls. or 714/736.ccls. or 714/737.ccls. or 714/741.ccls.) and (simulation or emulation or model\$5)) and ((transient\$1) same ((power or suppl\$3) and current\$1))	USPAT	OR	ON	2003/11/05 17:44
S35	10	((702/58.ccls. or 702/118.ccls. or 702/120.ccls. or 714/719.ccls. or 714/720.ccls. or 714/721.ccls. or 714/728.ccls. or 714/735.ccls. or 714/736.ccls. or 714/737.ccls. or 714/741.ccls.) and (simulation or emulation or model\$5)) and (((transient\$1) same (power or suppl\$3)) same current\$1))	USPAT	OR	ON	2003/11/05 17:44
S36	464	(test\$3 with simulat\$3 with (IC or (integrated adj circuit\$1)))	USPAT	OR	OFF	2003/11/05 17:48
S37	34	((test\$3 with simulat\$3 with (IC or (integrated adj circuit\$1)))) and ((power adj supply) with current\$1)	USPAT	OR	OFF	2003/11/05 17:52
S38	6	((test\$3 with simulat\$3 with (IC or (integrated adj circuit\$1)))) and (transient same ((power adj supply) with current\$1))	USPAT	OR	OFF	2003/11/05 18:18
S39	4	cadence.as. and CAE	USPAT	OR	OFF	2003/11/05 18:46
S40	4	quickturn.as. and CAE	USPAT	OR	OFF	2003/11/05 18:48
S41	91	(ISHIDA-MASAHIRO YAMAGUCHI-TAKAHIRO).in.	USPAT	OR	OFF	2003/11/05 18:49

S42	7	((ISHIDA-MASAHIRO YAMAGUCHI-TAKAHIRO).in.) and fault and simulation	USPAT	OR	OFF	2003/11/05 19:26
S43	2048	(signal with line) and (path with delay) and (gate with delay)	USPAT	OR	OFF	2003/11/05 19:32
S44	1	"6031386".pn.	USPAT	OR	OFF	2003/11/07 19:29
S51	148	fault adj list	USPAT	OR	OFF	2005/06/29 14:51
S52	18	S51 same sequence	USPAT	OR	OFF	2005/06/29 15:14
S53	3	S52 same gate	USPAT	OR	OFF	2005/06/29 15:42
S54	1	"6031386".pn.	USPAT	OR	OFF	2005/06/29 15:14
S55	245	transient same power same current same test\$4	USPAT	OR	OFF	2005/06/29 16:12
S56	10	S55 and ((logic with gate) same fault)	USPAT	OR	OFF	2005/06/29 15:44
S57	4	S56 and (fault with list)	USPAT	OR	OFF	2005/06/29 16:09
S58	77	(ISHIDA-MASAHIRO).in.	USPAT	OR	OFF	2005/06/29 16:11
S59	18	S58 and (YAMAGUCHI-TAKAHIRO HASHIMOTO-YOSHIRO HASHIMOTO-YOSHIROU).in.	USPAT	OR	OFF	2005/06/29 16:12
S60	5	S59 and (transient same power same current)	USPAT	OR	OFF	2005/06/29 16:13